# How Pencil could go GPU — a roadmap?

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#### Disregarded for today

- particles
- CPU parallelization

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#### from (ii)

- CUDA disfavored: tied to NVIDIA hardware, CUDA-Fortran not free
- OpenCL: a standard for programming of heterogeneous systems (GPU, multi-core CPU)
  - Pro: ∃ implementations for several platforms (NVIDIA, AMD, Intel)
    - ∃ FortranCL
       a free wrapper library, easy to extend
  - Con: performance perhaps worse vs. CUDA, implementation-dependent

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#### from (ii) from(iii): CUDA disfavored: manual rewriting of only a small tied to NVIDIA hardware, part of code acceptable CUDA-Fortran not free even FortranCL requires kernels to be written in C OpenCL: a standard for programming of heterogeneous $\rightarrow$ bulk of code transformation systems (GPU, multi-core CPU) to be done by a program Pro: ● ∃ implementations for several platforms (NVIDIA, AMD, Intel) ● ∃ FortranCL a free wrapper library, easy to extend Con: performance perhaps worse vs. CUDA, implementation-dependent

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#### from (iv)

- bulk of numerical work to be brought to GPU
  - $\Longrightarrow \pm$  everything inside time-loop
- pursue optimum use of GPU memory
- exploit CPU-GPU concurrency

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from (iv)	from (i) and (v):
<ul> <li>bulk of numerical work to be brought to GPU</li> <li>⇒ ± everything inside time-loop</li> </ul>	<ul> <li>no branching!</li> <li>a switchable GPU module,</li> <li>+ few if (lgpu) clauses</li> </ul>
<ul> <li>pursue optimum use of GPU memory</li> </ul>	<ul> <li>maintain code structure, in particular</li> </ul>
exploit CPU-GPU concurrency	<ul> <li>pencil concept</li> <li>switchable modules</li> <li>flexibility of spatial and temporal discretizations</li> </ul>

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### Pencil to GPU: A look at the code

#### Structure of time loop body (simplified)

```
while not <time limit>
  df = 0
  do i = 1, n_substeps
   apply boundary conditions on system variables in array f
     do n = 1, nzgrid
       do m = 1, nygrid
          calculate all pencils in structure p ("pencil case")
          use p for cumulatively constructing rhs of pde system
          in array df
       enddo
     enddo
     estimate optimum time step dt
     f = f + beta(i) * dt * df
  enddo
endwhile
```

#### Flow of data



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#### Sizes

input data: < 1000 scalars,</li>

< 100 1D arrays, size nxgrid, nygrid or nzgrid

- variable massive f: nxgrid × nygrid × nzgrid × number of variables
- pencil case p: nxgrid × number of pencils
- massive of rhs df: nxgrid × nygrid × nzgrid × nzgrid

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### Typical code

#### pencil calculation (calc\_pencils\_\* subroutines)

- if (lpencil(i\_uu)) p%uu = f(l1:l2,m,n,iux:iuz)
- if (lpencil(i\_u2)) call dot2\_mn(p%uu,p%u2)
- if (lpencil(i\_uij5)) call gij(f,iuu,p%uij5,5)
- if (lpencil(i\_sij)) call traceless\_strain

```
(p%uij,p%divu,p%sij,p%uu,lshear_rateofstrain)
```

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  - (p%uij,p%divu,p%sij,p%uu,lshear\_rateofstrain)

#### rhs calculation (d\*\_dtt subroutines)

 either copy from f or differential operation (on p or f) or linear combination of pencils; only a few (mostly scalar) input data used

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- $\implies$  simplest: parallelize on GPU w.r.t. *x* (pencil direction)
- rewrite all differential operations, including underlying difference formulae, as kernels; replace each operation on p and df by kernel call

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Pro:

- no need to copy input data to GPU, appear as actual parameters in kernel calls
- most code changes local, only modules Deriv and Sub need to be dubbed in (OpenCL-) C

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Con:

- number of threads = nxgrid, but should be  $\lesssim$  10,000
- use of shared memory for p, f, df ruled out as scope and lifetime limited to those of kernel
- $\implies$  optimum speedup not achievable

### PC transformation: Doing better

simplest: parallelize w.r.t. x and y with number of threads
 = nxgrid\* (nygrid/ystep)
 ⇒ p inflates by nygrid/ystep

ystep: from trade-off between number of threads and available shared memory

- ⇒ all code within mn loop to be converted in *one kernel* ⇒ much more/more complex code to be touched
- p and df should (can?) sit completely in shared memory f perhaps not, as access is not frequent (rare for calculation of p and df + once in each substep for integration) minimize by use of pencils

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 needed input data to be transferred into GPU's constant (global) memory

### PC transformation: Subtasks

- develop optimal recipe for shared memory use depending on device limitations and problem size
   (↗ "rolling cache", experience from other GPU codes)
- code modules Deriv and Sub (conservative!) in OpenCL-C; make use of specific vector data types; promote use of registers ( => avoid local arrays)
- comb manually through all calc\_pencils\_\* and d\*\_dtt subroutines and their callees for
  - purification from any nm constant operations
  - minimization of auxiliary arrays (favor in-place work)
  - removal of non-standard behavior (early f modification)

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- insertion of helper directives for code converter
- code a module GPU for initializing GPU use (FortranCL)
- create a code converter for calc\_pencils\_\* and d\*\_dtt subroutines and their callees

### PC transformation: The GPU module

#### Tasks

- determines GPU type and memory limitations
- determines optimum scheme of GPU memory use
- initializes GPU memory with input data (grid, pencil mask etc.) in constant memory,
- performs data exchange between CPU and GPU (global memory) for f array
- loads GPU program (precompiled OpenCL-C code)

### PC transformation: The code converter

#### Tasks

• generates C constant definitions for all compile-time constants, e.g. i<variable name>,

i\_<pencil name>,l<module name>

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(from cparam.f90, cparam.local, cparam.inc)

- generates C constant definitions for accessing pencil case p as an array (from cparam\_pencils.inc)
- parses for
  - output (usually informative) ⇒ remove
  - error handling  $\implies$  modify into "return with error code"
  - used input data: generate constant definitions, e.g. #define l\_<name> for any logicals l<name> #define i[xyz]\_<name> for any vector of dimension n[xyz]grid etc.

 $\implies$  generate array references

### PC transformation: The code converter

#### More tasks

- collects all calc\_pencils\_\* and d\*\_dtt calls into one kernel function (OpenCL C)
- transforms all callees into device functions
- generates code for declaration of global-scope data on GPU (avoids excessive parameter lists) and transfer from CPU to GPU (Fortran)
- replace global reduction operations by dedicated OpenCL library calls

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### PC transformation: The code converter

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#### How to be implemented?

- preprocessor Fortran to C converter postprocessor
- \*processor: Perl script?
- Fortran to C converter: open source?, yacc, lex ? (no experience)

### PC transformation: keep CPU busy

#### Concurrent with GPU calculations

- CPU–GPU data transfer
- application of boundary conditions
- I/O and related calculations
- calculation of diagnostics, in particular averages

via non-blocking OpenCL command queue

## Proposals most welcome !!!

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